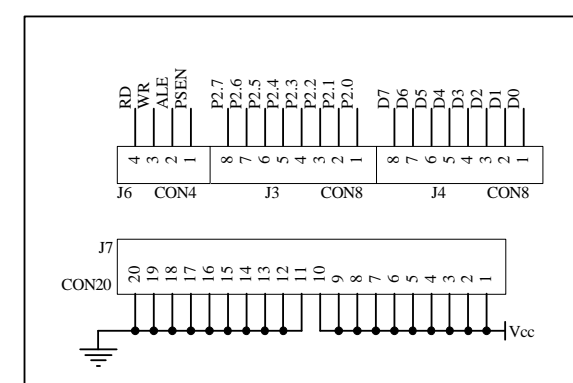
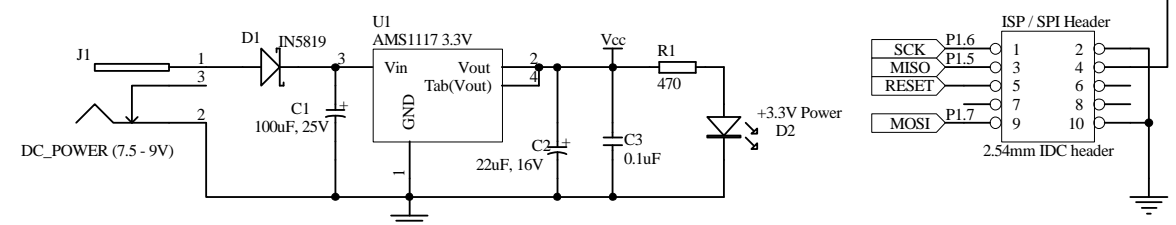


ISP downloader port for FLIP; the free Atmel software

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This evaluation board has been designed by using the same PCB of another development board: AT89S52-gLCD-STK1
 We simply replace the AMS1117-5V with AM1117-3.3V linear regulator to obtain Vcc at 3.3V
 Vcc=3.3V allows us to use a 65k color LCD module (CM60010ACT6_BO)
 The microcontroller has been replaced by AT89C51RB2 for sack of the capability of running at Vcc=3.3V and using FLIP
 for ISP programming, thus no ISP downloader hardware is required

Development Board : AT89C51RB2_3V3_EVAL
 Revision a
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