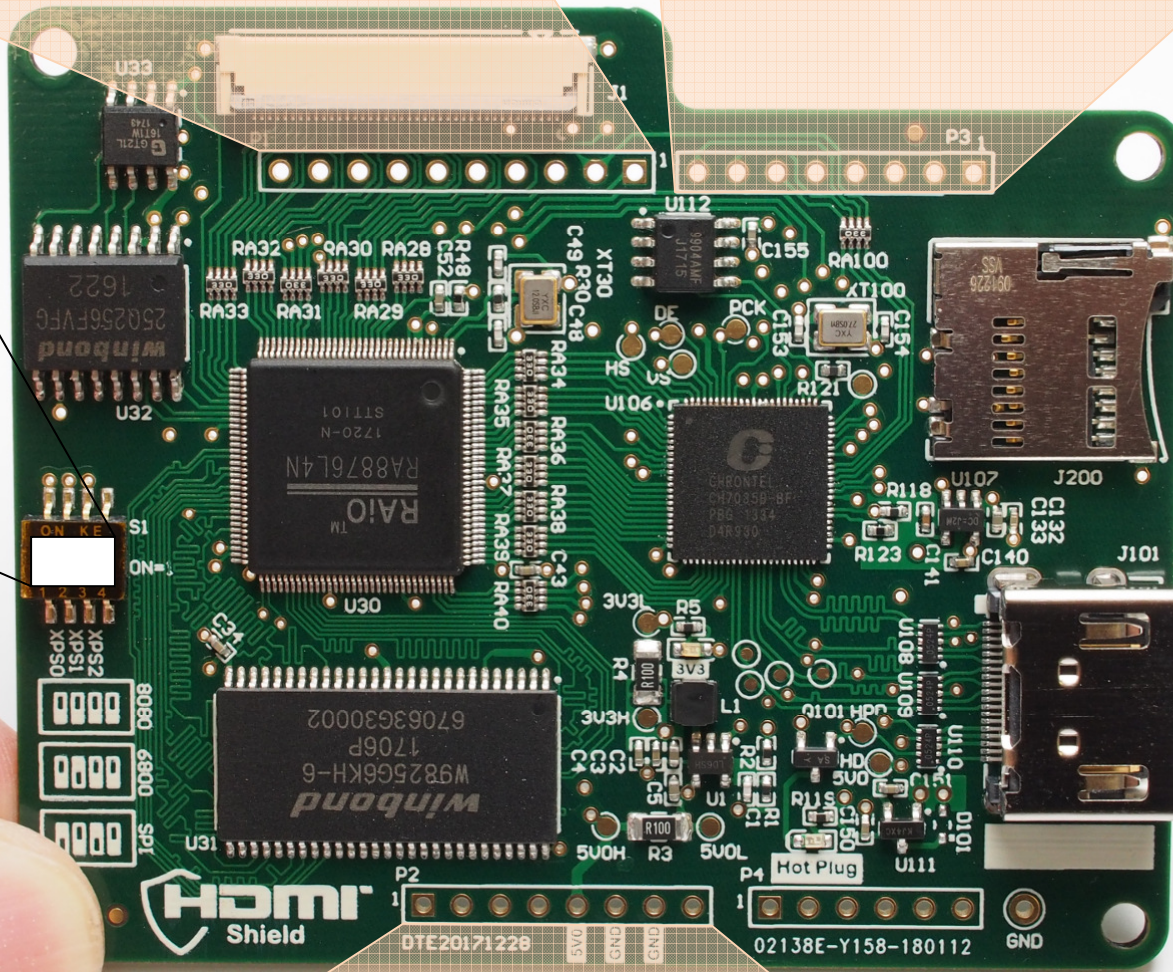
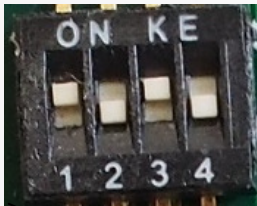


CH	CH	NC	GND	SPI	SPI	SPI	SPI	CH	XnRST
SDA	SCL			SCK	MISO	MOSI	CS	I2S_S0	

CH	CH	CH	SD	XnINTR	NC	NC	NC
I2S_LRCLK	I2S_SCLK	IRQ	CS				

Set S1 to 1,3=ON, 2,4=OFF for SPI



NC	NC	NC	NC	NC	5V	GND	NC
	NC	NC	NC	NC	IN	GND	

Remarks

* Power input: 5V@500mA to pin 5 of header P2

* Board has 3.3V regulator onboard.
3.3V levels for all I2C, SPI, and GPIOs.

Inputs are NOT 5V compatible. Use 3.3V please!

* Cells labeled with color for CH7035B interface

* Cells labeled with color for RA8876 interface

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