



P1

HSYNC/CS#	1	2	PCLK/RD#
VSYNC/WR#	3	4	CM
DEN/DC	5	6	SHUT
DATA1/B1	7	8	DATA0/B0
DATA3/B3	9	10	DATA2/B2
DATA5/G0	11	12	DATA4/B4
DATA7/G2	13	14	DATA6/G1
DATA9/G4	15	16	DATA8/G3
DATA11/R0	17	18	DATA10/G5
DATA13/R2	19	20	DATA12/R1
DATA15/R4	21	22	DATA16
DATA17	23	24	DATA18
DATA19	25	26	DATA20
DATA21	27	28	DATA22
DATA23	29	30	DATA22
GND	31	32	SD/C#
DOUT	33	34	SCSX
DIN	35	36	SCK
INT#	37	38	TE
VDDIO_CTRL	39	40	MCU_RST

Test Points in 2.54mm Header 20X2

P2

1	2	SDA
3	4	GPIO ⁺
5	6	GPIO ⁺
7	8	INT [*]
9	10	GPIO ⁺
11	12	GPIO ⁺
13	14	GPIO ⁺
15	16	UxRX
17	18	SCLK
19	20	MISO
21	22	SS
23	24	GPIO ⁺
25	26	GPIO ⁺
27	28	GPIO ⁺
29	30	GPIO ⁺
31	32	GPIO ⁺
33	34	GPIO ⁺
35	36	GPIO ⁺
37	38	GPIO ⁺
39	40	GPIO ⁺

Test Points in 2.54mm Header 20X2

