



**P1**

1	HSYNC/CS#	1	PCLK/RD#
2	VSYNC/WR#	2	CM
3	DEN/DC	3	SHUT
4	DATA1/B1	4	DATA0/B0
5	DATA3/B3	5	DATA2/B2
6	DATA5/G0	6	DATA4/B4
7	DATA7/G2	7	DATA6/G1
8	DATA9/G4	8	DATA8/G3
9	DATA11/R0	9	DATA10/G5
10	DATA13/R2	10	DATA12/R1
11	DATA15/R4	11	DATA14/R3
12	DATA17	12	DATA16
13	DATA19	13	DATA18
14	DATA21	14	DATA20
15	DATA23	15	DATA22
16	DOUT	16	SD/C#
17	DIN	17	SCSX
18	INT#	18	SCK
19	VDDIO_CTLR	19	TE
20		20	MCU_RST

Test Points in 2.54mm Header 20X2

**P2**

1	SDA	1	SCL
2	GPIO^	2	GPIO^^
3	GPIO^	3	GPIO^^
4	INT*	4	GPIO*
5	GPIO*	5	GPIO**
6	GPIO**	6	GPIO**
7	UxRX	7	UxTX
8	UxRX	8	UxTX
9	PWM	9	PWM
10	SCLK	10	SCLK
11	INT*	11	INT*
12	GPIO*	12	GPIO*
13	GPIO**	13	GPIO**
14	GPIO**	14	GPIO**
15	UxRX	15	UxTX
16	UxRX	16	UxTX
17	PWM	17	PWM
18	SCLK	18	SCLK
19	MISO	19	MISO
20	SS	20	SS
21	SCLK	21	SCLK
22	MISO	22	MISO
23	MISO	23	MISO
24	EXT_5V	24	EXT_5V
25	VDDIO	25	VDDIO
26	VDDIO	26	VDDIO
27	VDDIO	27	VDDIO
28	VDDIO	28	VDDIO
29	VDDIO	29	VDDIO
30	VDDIO	30	VDDIO
31	VDDIO	31	VDDIO
32	VDDIO	32	VDDIO
33	VDDIO	33	VDDIO
34	VDDIO	34	VDDIO
35	VDDIO	35	VDDIO
36	VDDIO	36	VDDIO
37	VDDIO	37	VDDIO
38	VDDIO	38	VDDIO
39	VDDIO	39	VDDIO
40	VDDIO	40	VDDIO

Test Points in 2.54mm Header 20X2