

Product Specification

1. GENERAL DESCRIPTION

The LH154Q01 is a Color Active Matrix Liquid Crystal Display with Light Emission Diode(LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is transmissive type display operating in the normally white mode. This TFT-LCD has 1.54 inch diagonally measured active display area with (240*RGB*240) resolution. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes.

Block Diagram

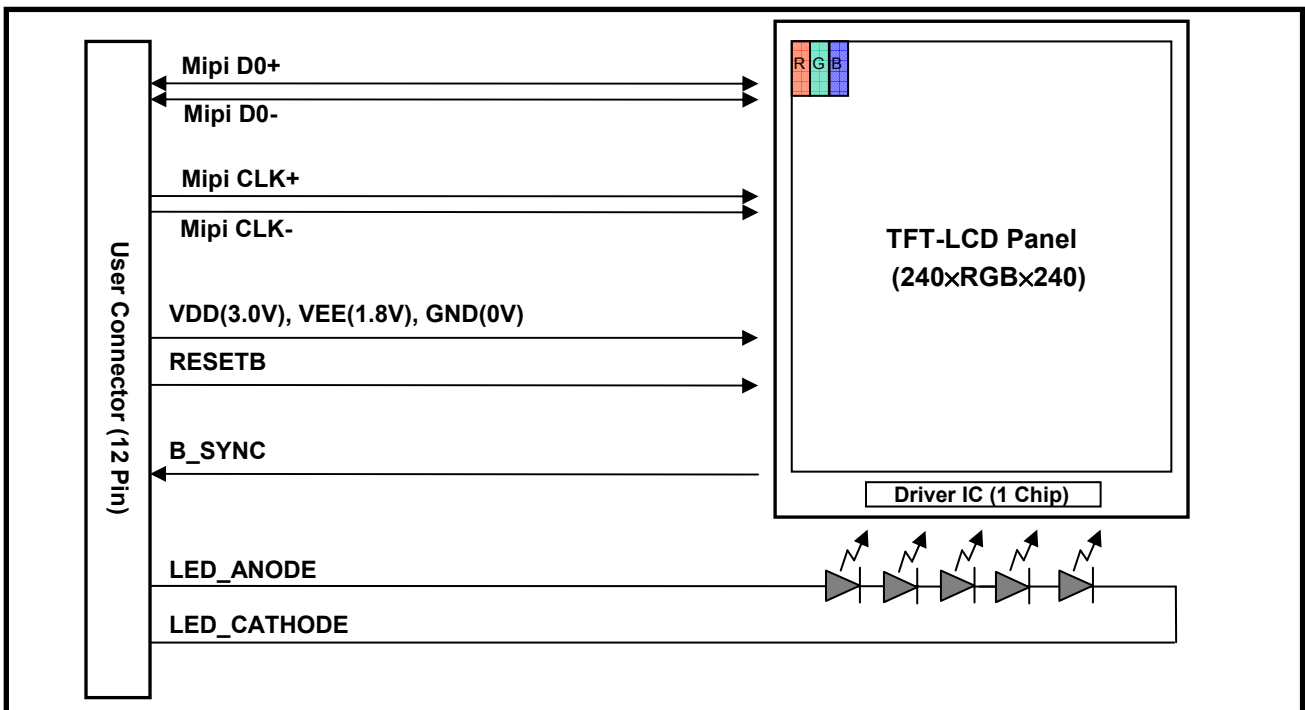


Fig 1.1 Block Diagram of TFT-LCD Module with LED Backlight Unit

General Features

Item	Specification
Active Screen Size	1.54" diagonal
Outline Dimension	31.82 (H) x 33.72 (V) x 1.147 (T) Typ.
Pixel Pitch	0.1155(H) x 0.1155(V)
Pixel Format	240(H) X 240 (V) (RGB Stripe)
Color Depth	18-bits (R6, G6, B6)
Interface	MIPI 1-lane 24-bits (D-PHY version 0.92, DSI version 1.01 r11)
Power Consumption	205mW (max. B/L on @ 10.5mA), 25mW (max. B/L off)
Luminance	450nit(typ.) @ 10.5mA
Viewing Direction	6:30 o'clock (Non-inversion)

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2. ABSOLUTE MAXIMUM RATINGS

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 2.1 Absolute Maximum Ratings

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Supply Input	VDD	-0.3	4.0	V	
Power Supply Input	VEE	-0.3	4.0	V	
LED Current	I_{LED}	-	25	mA	1, 2

Notes:

1. Applies to each LED individually.
2. Allowable forward current is refer to Fig 2.1

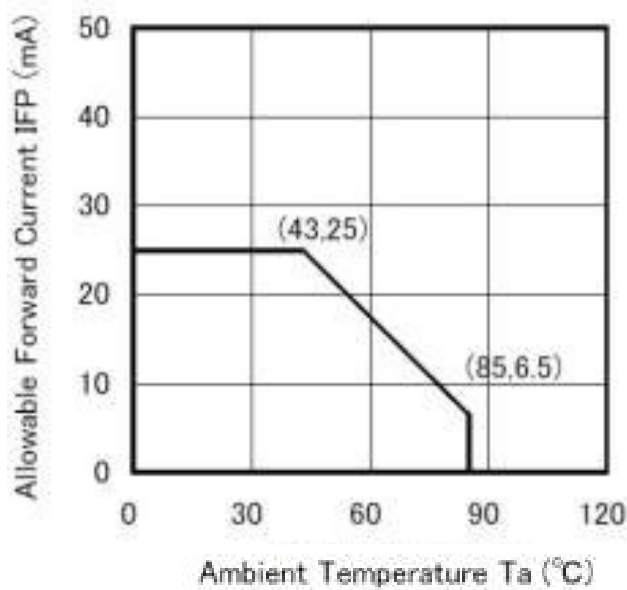


Fig 2.1 Ambient Temperature vs. Allowable Forward Current

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3. ELECTRICAL SPECIFICATIONS

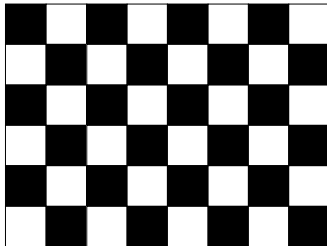
3-1. ELECTRICAL CHARACTERISTICS

Table 3.1 Electrical Characteristics Of TFT-LCD Module

Parameter	Symbol	Values			Units	Notes
		Min	Typ	Max		
Power Supply Input (Analog)	VDD	2.9	3.0	3.1	V	
Power Supply Input (Digital)	VEE	1.7	1.8	1.9	V	
“H”Level Input Voltage	V _{IH}	0.8 V _{EE}	-	-	V	
“L”Level Input Voltage	V _{IL}	-	-	0.2 V _{EE}	V	
Power Consumption, Panel	P _B		20	25	mW	1

Notes:

1. Large black/white checker pattern(20 pixel blocks) at 60Hz



White : 64Gray
Black : 0Gray

3-2. BACK LIGHT UNIT

The edge-lighting type of back light unit consists of 5 LEDs which is connected in serial.

Table 3.2 Electrical Characteristics Of Back Light Unit

Parameter	Symbol	Values			Units	Notes
		Min	Typ.	Max		
LED Current	I _{LED}	-	10.5	25	mA	
LED Forward Voltage	V _{LED}	-	15.5	17.0	V	

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3-3. INTERFACE CONNECTIONS

LCD Connector: 503552-1220 (Molex)

System Mating Connector: 503548-1220 (Molex)

Table 3.3 Module Connector Pin Configuration

Pin	Signal	I/O	Description
1	CLKP	I/O	MIPI Clock
2	VDD	-	3.0V Power Supply
3	CLKN	I/O	MIPI Clock
4	VEE	-	1.8V Power Supply
5	GND	-	Ground
6	B_Sync	O	Synchronization Pulse Signal
7	D0P	I/O	MIPI Data
8	Reset	I	Reset (Active Low)
9	D0N	I/O	MIPI Data
10	LED+	O	LED Anode
11	GND	-	Ground
12	LED-	O	LED Cathode

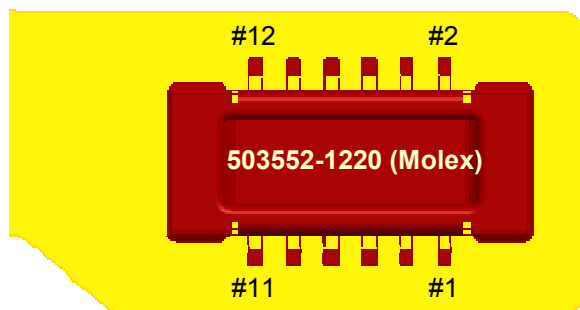


Fig 3.1 Connector Diagram

Note:

1. All GND(ground) pins should be connected together.

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3-4. COLOR INPUT DATA REFERENCE

Table 3.4 Color vs. Data

Display Colors		Data Signal																											
		R 0	R 1	R 2	R 3	R 4	R 5	R 6	R 7	G 0	G 1	G 2	G 3	G 4	G 5	G 6	G 7	B 0	B 1	B 2	B 3	B 4	B 5	B 6	B 7				
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	
	Green	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	1	1	1	1	1	1	1	1	
	Red	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Magenta	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
	Yellow	0	0	1	1	1	1	1	1	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	White	0	0	1	1	1	1	1	1	0	0	1	1	1	1	1	1	1	0	0	1	1	1	1	1	1	1	1	
Red Gray Scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	GS1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Darker ↑ ↓ Brighter	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
		0	0	•	•	•	•	•	•	0	0	•	•	•	•	•	•	•	0	0	•	•	•	•	•	•	•		
	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	GS62	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Red	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
Green Gray Scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	GS1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Darker ↑ ↓ Brighter	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
		0	0	•	•	•	•	•	•	0	0	•	•	•	•	•	•	•	0	0	•	•	•	•	•	•	•		
	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0		
	GS62	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0		
	Green	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0		
Blue Gray Scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0		
	Darker ↑ ↓ Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0		
		0	0	•	•	•	•	•	•	0	0	•	•	•	•	•	•	•	0	0	•	•	•	•	•	•	•		
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1		
	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1		
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1		

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3-5. Power On/Off Sequence

Power On Sequence

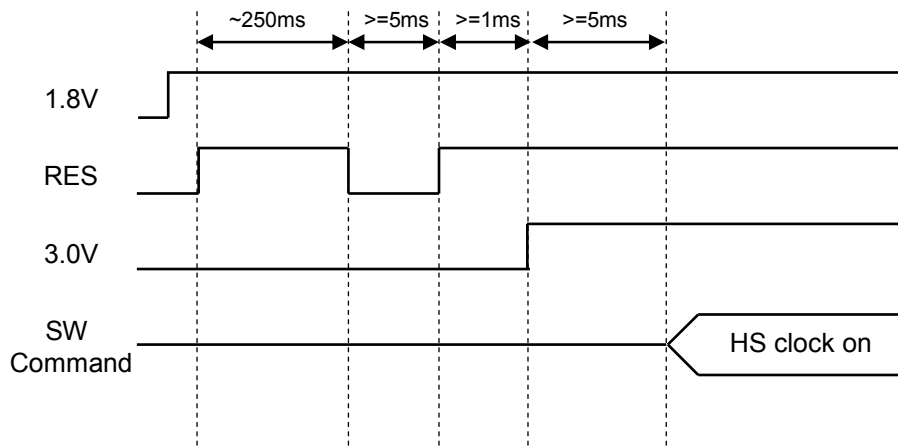


Fig 3.2 Power On Requirements

Power Off Sequence

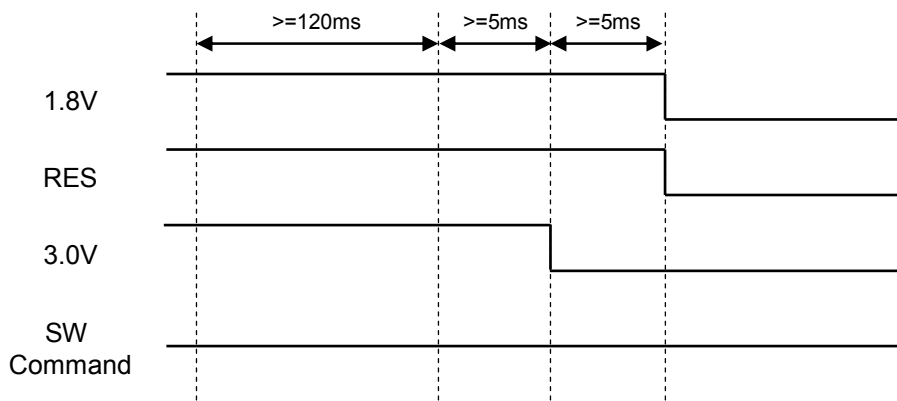


Fig 3.3 Power Off Requirements

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3-7. Software Flow

Power on				
Step	Register	Data	Delay	Command
1				VEE on (Typ 1.8V)
			10us	
2				H/W reset set to HIGH
			1ms	D-IC Logic power settlement
3				VDD on (Typ 3.0V)
			5ms	OSC stabilization & NVM loading
4				Turn on high-speed clock (HS clock on)
			10us	For settlement
5	0x11			Sleep Out
			120ms	
			40ms	Wait 2 frames
6	0x36	0x08		RGB/BGR order change
7	0x2C	Image		Start to send image data (HS data on)
8	0x29			Display On
9				Turn on Backlight

Power off				
Step	Register	Data	Delay	Command
1				Turn off Backlight
			1ms	
2	0x28			Display off
			5ms	
3	0x10			Sleep In
			120ms	Discharge time
4				Stop to send image data (HS data off)
5				Turn off high-speed clock (HS clock off)
			10us	
6				VDD off (Typ 3.0V)
			5ms	For settlement
7				HW reset set to LOW
			5ms	
8				VEE off (Typ 1.8V)

Table 3.5 Software Flowchart

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4. OPTICAL CHARACTERISTICS

4-1. Optical Characteristics – Backlight On

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Viewing Angle Range	ΘUP	CR ≥10	40	50		°(degree)	Note 3
	ΘDOWN		40	50		°(degree)	Note 3
	ΘLEFT		40	50		°(degree)	Note 3
	ΘRIGHT		40	50		°(degree)	Note 3
Contrast Ratio	CR	Optimal	100	150		--	Note 2
Brightness	Y	I _{LED} = 10.5mA	400	450		cd/m ²	Note 1 [PR880]
Brightness Uniformity	Y	I _{LED} =10.5mA	80			%	Note 5 [PR880]
Flicker	F	Optimal			10	%	Note 6
Response Time	τ _f + τ _r	Θ =0 ° Ta =25 °C		35	50	ms	Note 4
White Chromaticity	Wx	Θ =0 ° Ta =25 °C		0.309			Note 1 [PR650]
	Wy			0.324			
Red Chromaticity	Rx			0.610			
	Ry			0.345			
Green Chromaticity	Gx			0.320			
	Gy			0.555			
Blue Chromaticity	Bx			0.150			
	By			0.120			
Color Gamut	NTSC			50		%	

1. Optical Test Equipment & Method Refer to Note 1,2,3,4,5,6.

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[Note 1] Optical Test Equipment Setup

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface. In case of backlight on, measured on the center area of the panel by PHOTO RESEARCH photometer PR-880&PR650 or Equivalent.

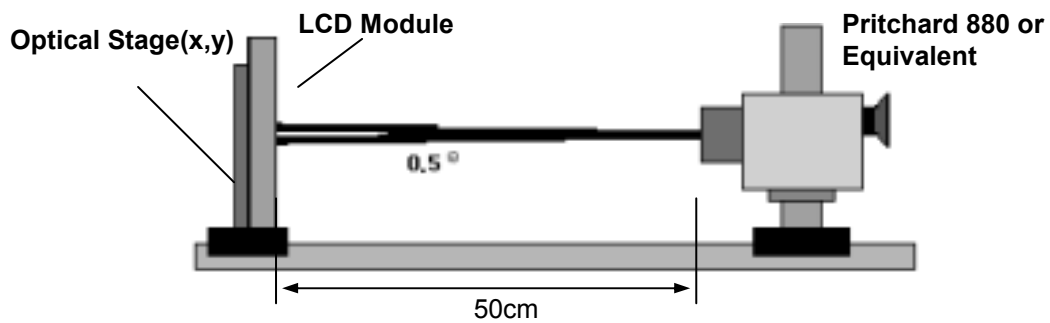


Fig 4.1 Backlight On (Optical Characteristic Measurement Equipment and Method)

[Note 2]

Contrast Ratio is defined as follows ;

$$\text{Contrast Ratio(CR)} = \frac{\text{Photo detector output with LCD being "White"}}{\text{Photo detector output with LCD being "Black"}}$$

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[Note 3]

Viewing Angle Range is defined as follows;

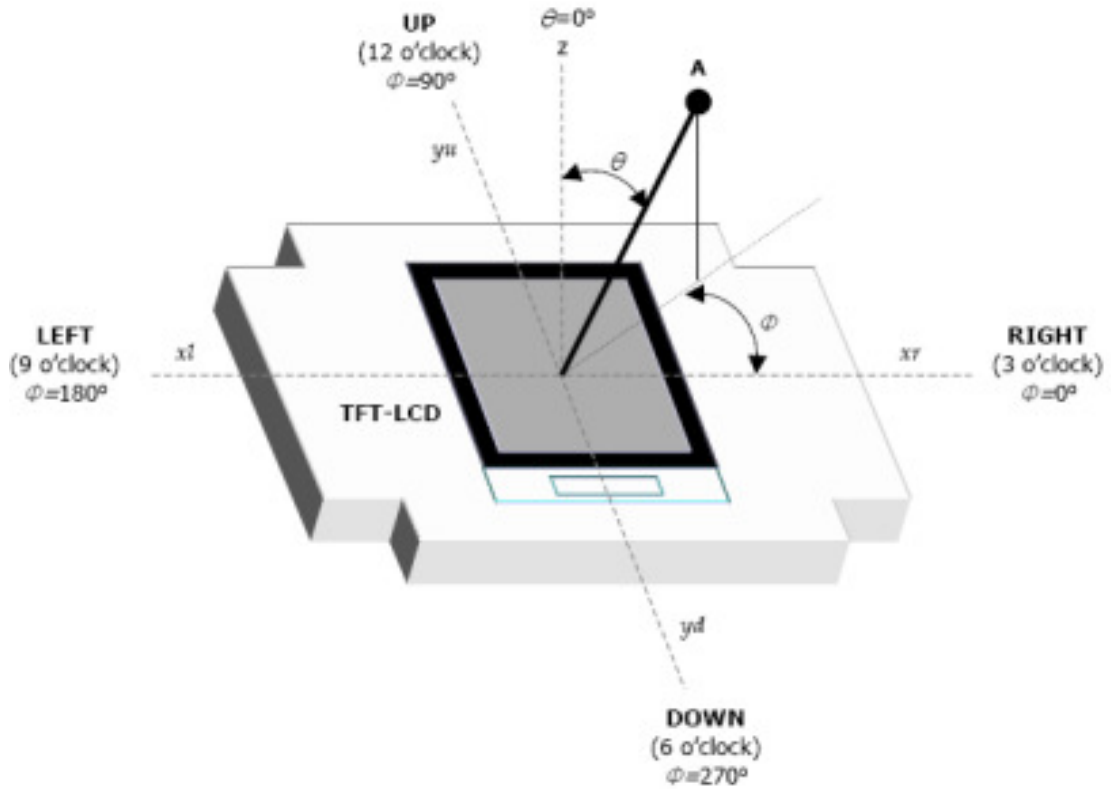


Fig 4.2 Viewing Angle Definitions

[Note 4]

Response time is obtained by measuring the transition time of photo detector output, when input signals are applied so as to make the area “black” to and from “white”.

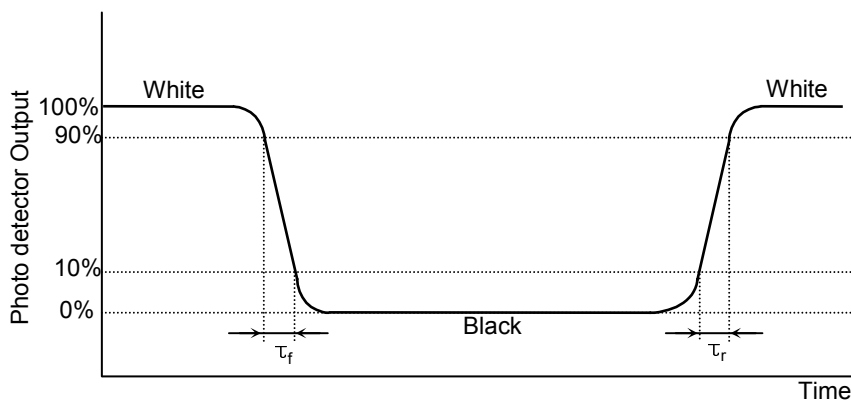


Fig 4.3 Response Time Definition

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[Note 5]

The brightness measurement is taken at point B5.

$$\text{Brightness Uniformity} = \frac{\text{Minimum photo detector output for B1-B9 with all pixels white}}{\text{Maximum photo detector output for B1-B9 with all pixels white}} \times 100$$

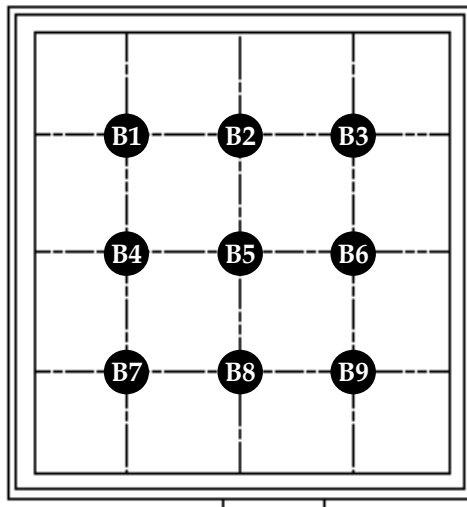


Fig 4.4 Brightness Measurement Points

[Note 6]

The Flicker measurement is taken at center area of the panel (B5).
Measurement equipment is YOKOGAWA 3298.
Measurement patten is Black and Middle gray horizontal.

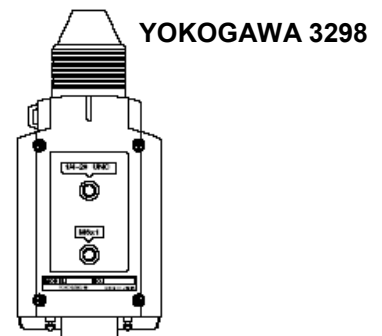
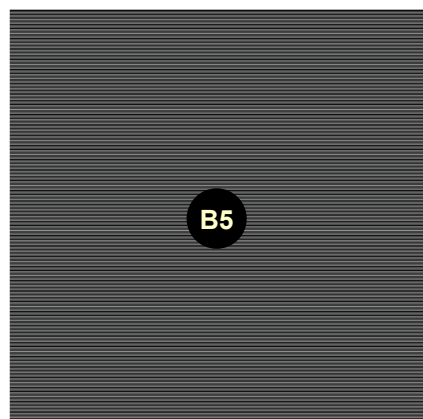


Fig 4.5 Flicker Measurement Points

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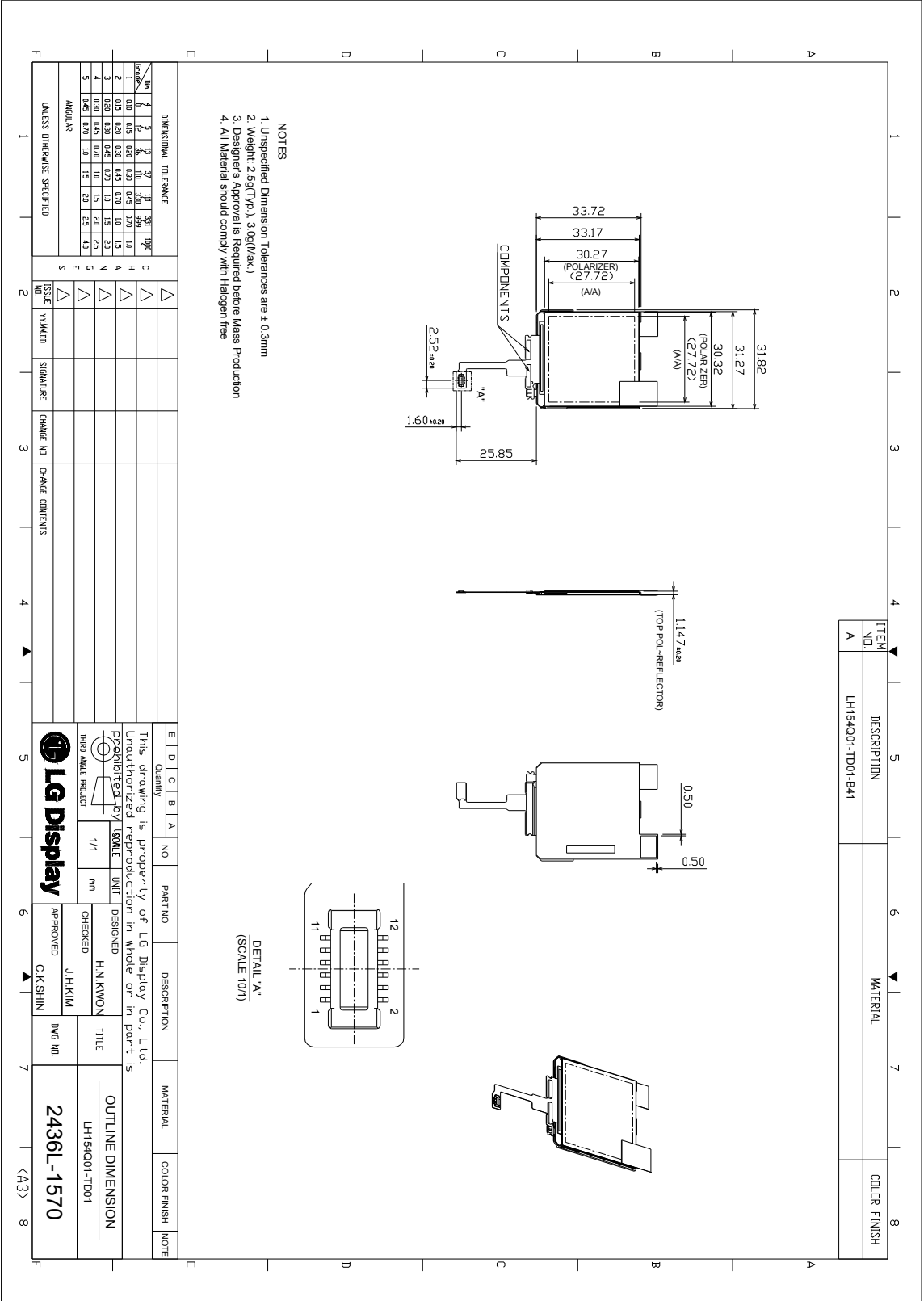
5. MECHANICAL CHARACTERISTICS

The contents provide general mechanical characteristics for the model.
In addition the figures in the next page are detailed mechanical drawing of the LCD.

DIMENSION	MIN	TYP	MAX	UNIT
HORIZONTAL	31.52	31.82	32.12	mm
VERTICAL	33.42	33.72	34.02	mm
THICKNESS	0.947	1.147	1.347	mm

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[Outline Dimension]



- NOTES
1. Unspecified Dimension Tolerances are ± 0.3mm
 2. Weight: 2.5g(Typ.), 3.0g(Max.)
 3. Designer's Approval is Required before Mass Production
 4. All Material should comply with Halogen free

UNLESS OTHERWISE SPECIFIED

ANGLE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30																													
1	0.10	0.15	0.20	0.25	0.30	0.35	0.40	0.45	0.50	0.55	0.60	0.65	0.70	0.75	0.80	0.85	0.90	0.95	1.00	1.05	1.10	1.15	1.20	1.25	1.30	1.35	1.40	1.45	1.50	1.55	1.60	1.65	1.70	1.75	1.80	1.85	1.90	1.95	2.00	2.05	2.10	2.15	2.20	2.25	2.30	2.35	2.40	2.45	2.50	2.55	2.60	2.65	2.70	2.75	2.80	2.85	2.90	2.95	3.00

ITEM NO.	DESCRIPTION	DATE	BY	CHKD.	APPV.
1	ISSUE				
2	YKAMD				
3	SIGNATURE				
4	CHANGE NO.				
5	CHANGE CONTENTS				

QTY	UNIT	PART NO.	DESCRIPTION	MATERIAL	COLOR FINISH	NOTE
1	mm		OUTLINE DIMENSION	LH154Q01-TD01		

2436L-1570 <A3>